

667070" 9884650

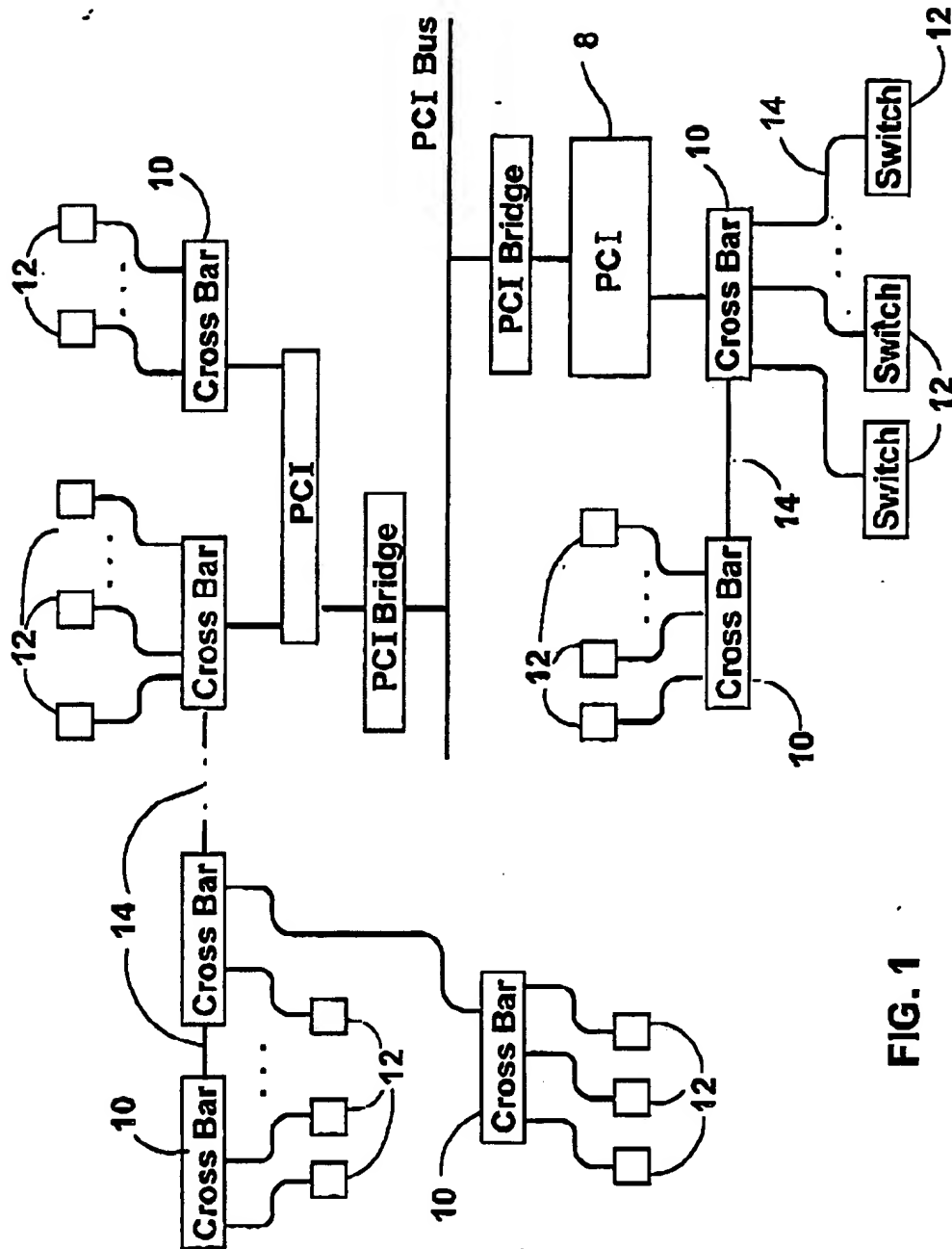


FIG. 1

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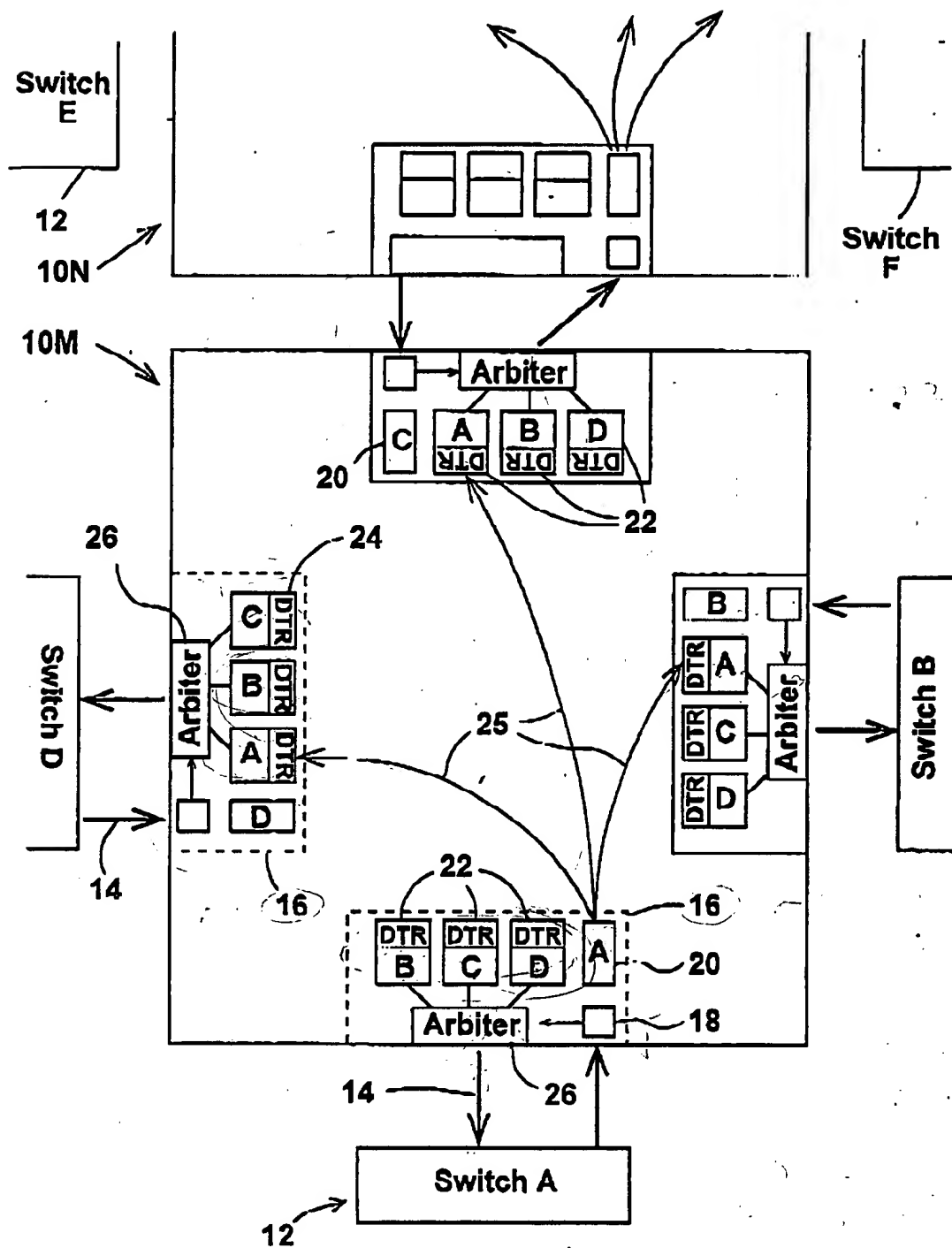


FIG. 2

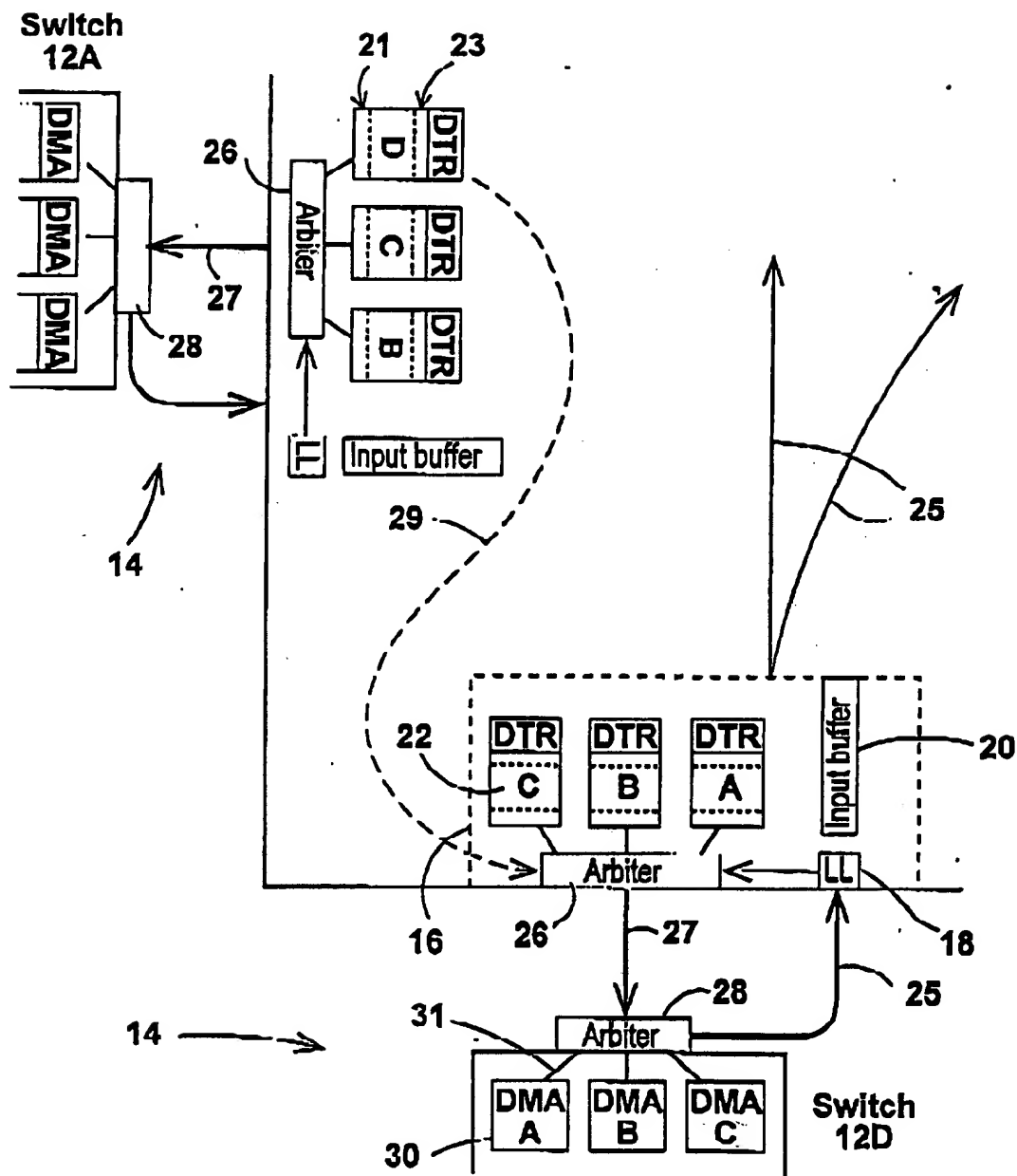


FIG. 3

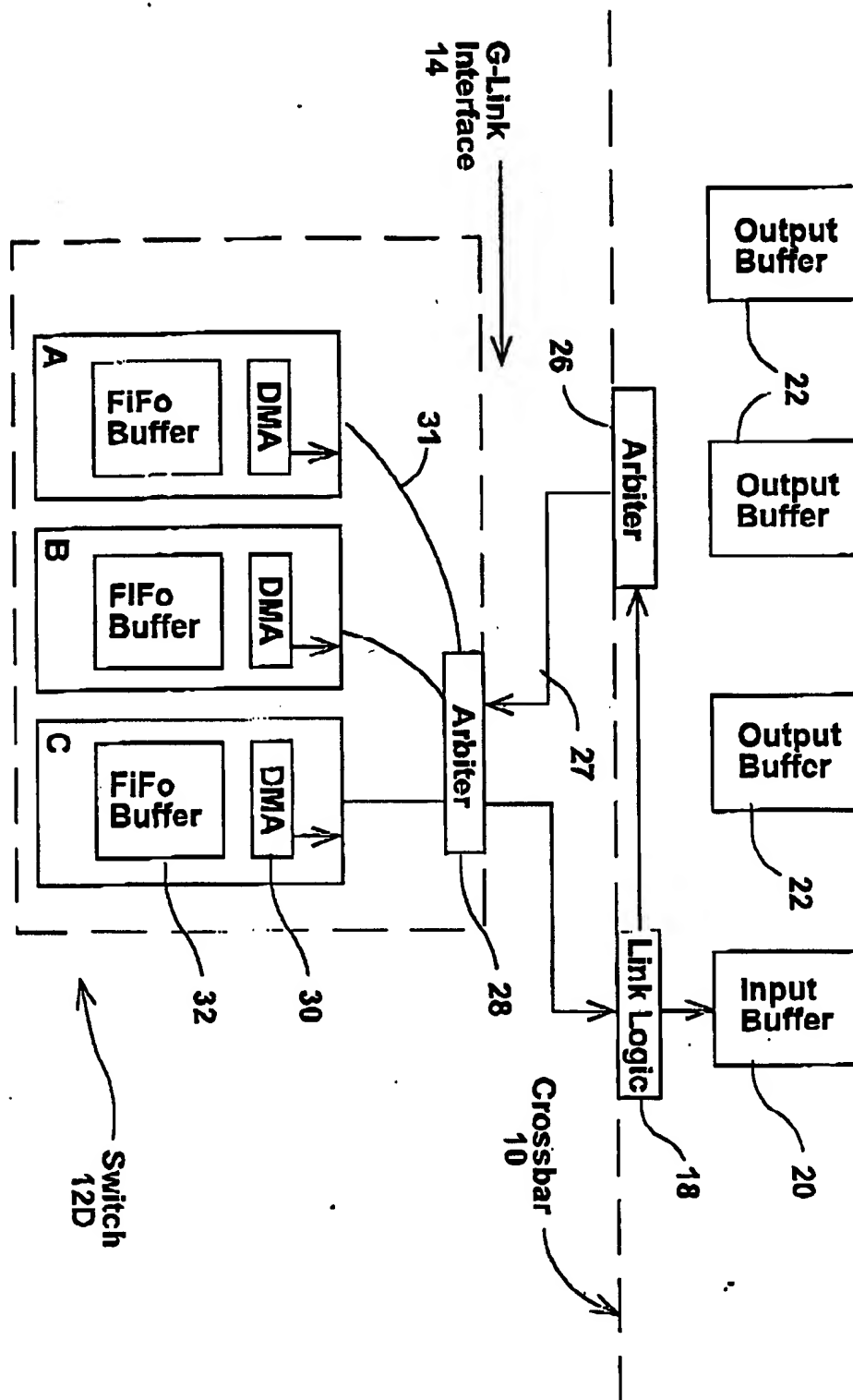


FIG. 4

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Source Network Switch 12A Port 16A on Cross Bar 10 16B on Cross Bar 10 Destination Network Switch 12B

110-Transfer a link message and data packet

112-Logic Unit 18A identifies link message as switch link message

114-Write link message and data packet into Input Buffer 20A

116-Transfer link message and data packet to Registrar 24A to Ports 16B, 16C and 16D

118-Receive message and data at registrars 24A at ports 16B, 16C and 16D

120-Message and data not recognize and ignore by registrars 24A at ports 16C and 16D

124-Write message and data into output buffer 22A

122-Message and data recognize by registrar 14A at ports 16B

126-Transfer data from output buffer 22B to cross bar arbiter 26B

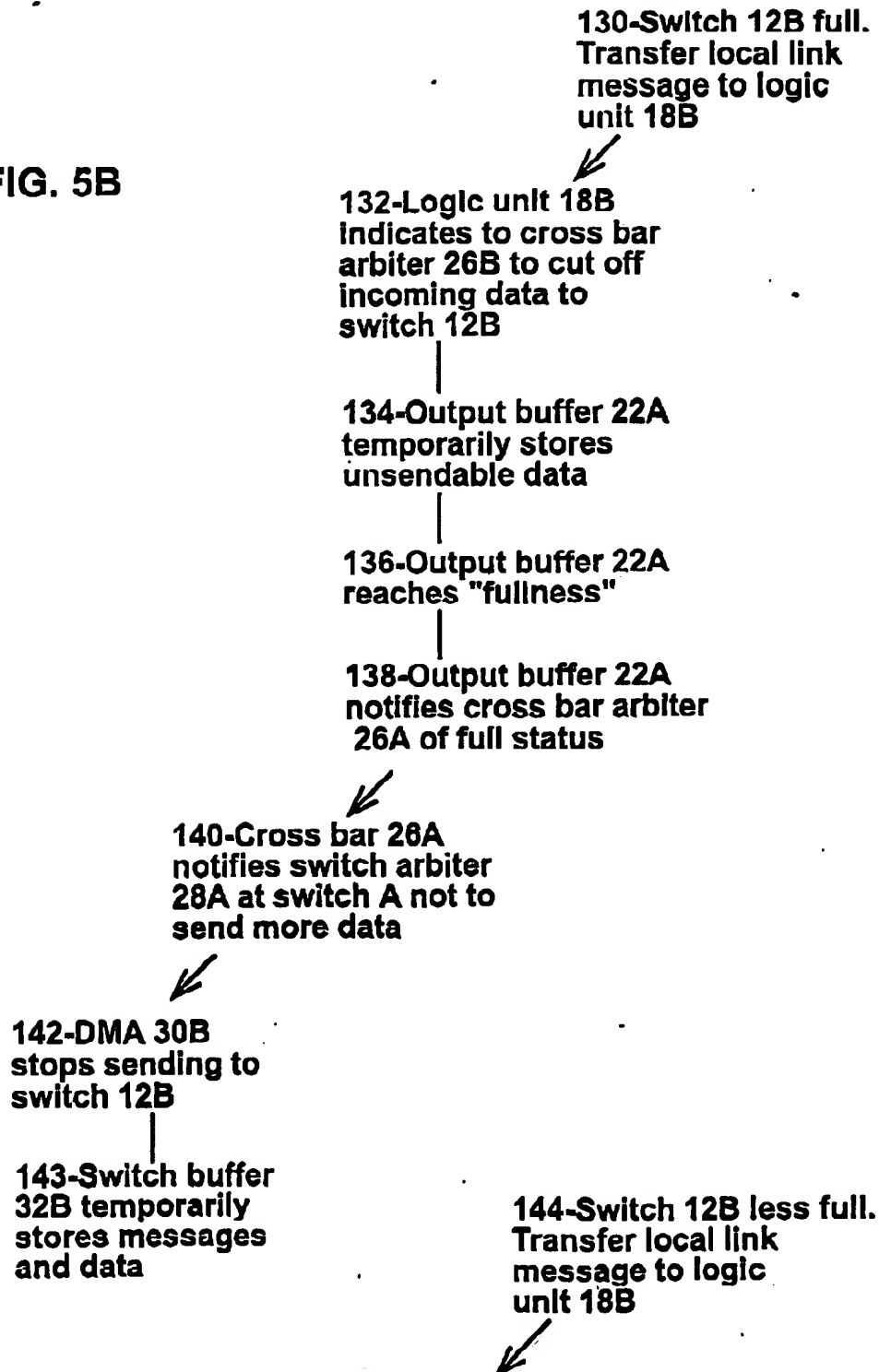
128-Transfer data from cross bar arbiter 26B via link 14B to switch 12B

FIG. 5A

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Source Network Switch 12A	Port 16A on Cross Bar 10	16B on Cross Bar 10	Destination Network Switch 12B
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FIG. 5B



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Source Network Switch 12A	Port 16A on Cross Bar 10	16B on Cross Bar 10	Destination Network Switch 12B
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146-Logic unit
18B indicates to
cross bar arbiter
26B to reopen
inflow to
switch 12B

148-Output
buffer 22A restarts
transmission to
switch 12B

150-Output
buffer 22A reaches
emptiness, notifies
cross bar arbiter 26A

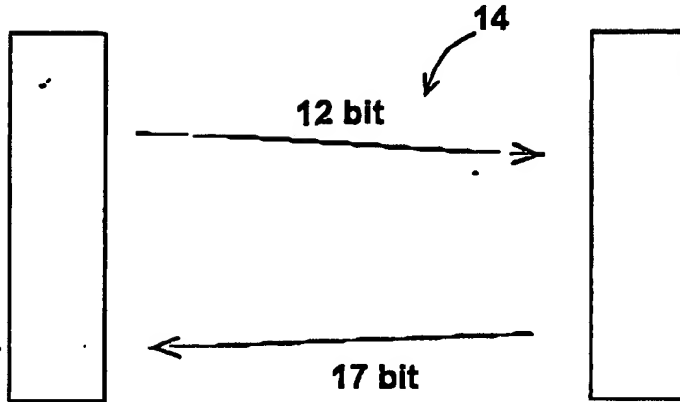
152-Cross bar
arbiter 28A notifies
switch arbiter 26A
to resume
transfer

154-Switch 12A
indicates to DMA
30B to resume
communication

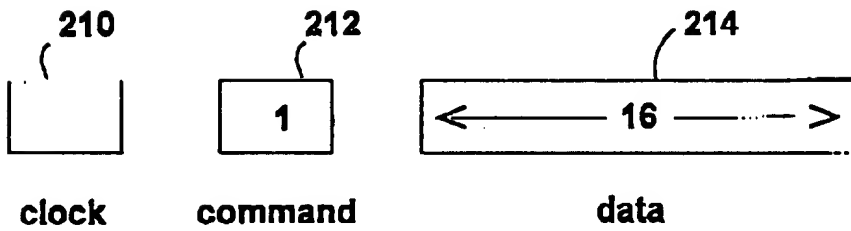
156-Switch buffer 32B
resumes sending
data

FIG. 5B Continued

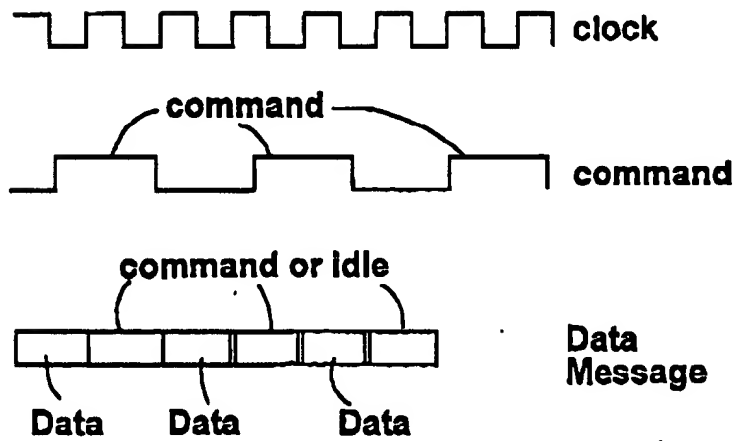
G - Link Interface



(A)



(B)



(C)

FIG. 6

Data Message

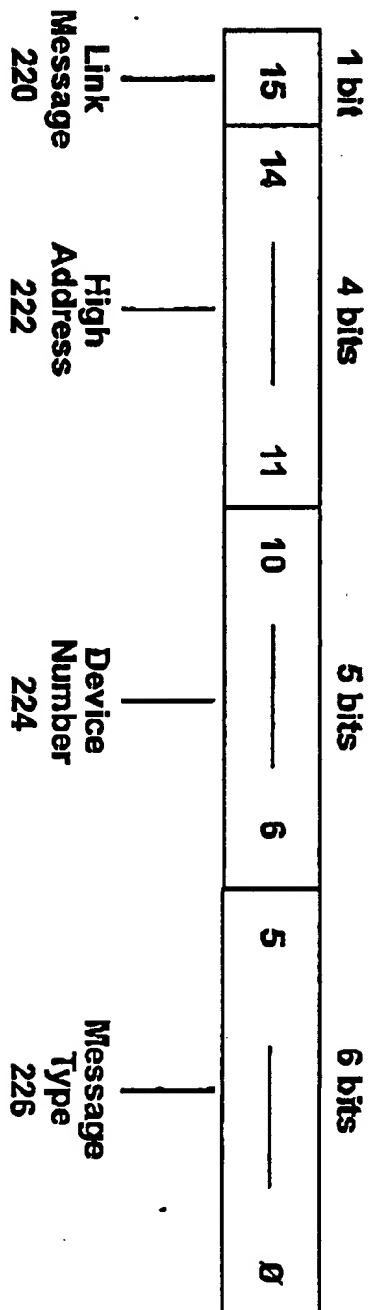


FIG. 7

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